

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	23372	((clock with (stabilization or stabl\$4)) or (signal with lock\$4)) and multiplex\$3	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/05/05 08:22
L2	12	(lcm with signal) and 1	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/05/05 08:28
L4	6977	(clock with detect\$4) and 1	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/05/05 08:23
L5	7091	((clock with (stabilization or stabl\$4)) or (signal with lock\$4)) and (state with machine)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/05/05 08:23

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L6	2325	(clock with detect\$4) and 5	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/05/05 08:23
L7	83	6 and (digital with clock with module) and (select\$4 with (circuit or multiplex\$3))	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/05/05 08:29
L8	53	7 and (buffer same feedback same signal)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/05/05 08:30
L10	53	8 and (input\$4 with signal) and (output\$4 with signal)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/05/05 08:30

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L15	52	10 and (programmable with logic\$2)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/05/05 08:34
L16	52	15 and (state with machine same register\$2)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/05/05 08:48
L18	52	(feedback with signal) and (generat\$4 with control\$4 with signal) and 16	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/05/05 09:24
L20	12575	((703/13-22) or (716/1-21)).CCLS.	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	OFF	2006/05/05 09:42

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L22	375	20 AND (Clock with detect\$4)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/05/05 09:43
L23	156	22 and (control\$4 adj2 signal) and (signal with generat\$4)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/05/05 09:44
L24	62	23 and (state with machine)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/05/05 09:44
L25	53	24 and (input\$4 with signal) and (output\$4 with signal) and register and multiplex\$3	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/05/05 09:45

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L26	6	25 and (receiv\$4 with input\$4) and (lock with signal)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/05/05 09:46
L27	13	25 and (receiv\$4 with input\$4) and (lock\$4 with signal)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/05/05 10:03
L28	1	25 and (receiv\$4 with input\$4) and (lock\$4 with feedback with signal)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/05/05 09:47
L29	2	27 and (receiv\$4 with feedback with signal)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2006/05/05 11:46

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L30	1	29 and (receiv\$4 with control with signal)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/05 10:38
L31	2	27 and (lock adj signal\$2)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/05 10:42
L33	1	29 and (common adj signal\$2)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/05 10:43